

TOSHIBA Bipolar Linear Integrated Circuit Silicon Monolithic

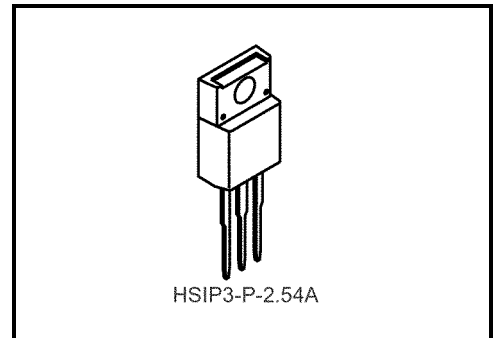
TA79005S,TA79006S,TA79007S,TA79008S,TA79009S,TA79010S, TA79012S,TA79015S,TA79018S,TA79020S,TA79024S

Output Current of 1A, Three-Terminal Negative Voltage Regulators

-5 V, -6 V, -7 V, -8 V, -9 V, -10 V, -12 V, -15 V, -18 V, -20 V, -24 V

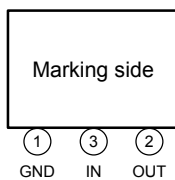
Features

- Suitable for CMOS, TTL, and the other digital IC power supply.
- Internal thermal overload protecting.
- Internal short circuit current limiting.
- Maximum output current of 1.0 A.
- Metal fin (tab) is fully covered with mold resin.
(TO-220 NIS package)

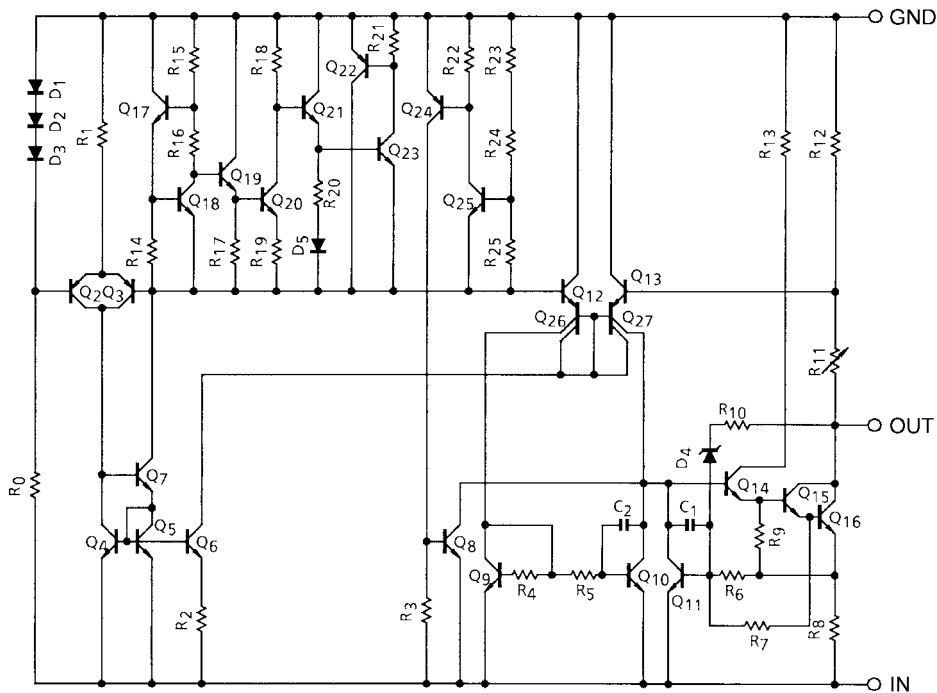


Weight: 1.7 g (typ.)

Pin Assignment



Equivalent Circuit



Maximum Ratings (Ta = 25°C)

Characteristics		Symbol	Rating	Unit
Input voltage	TA79005S	V _{IN}	-35	V
	TA79006S			
	TA79007S			
	TA79008S			
	TA79009S			
	TA79010S			
	TA79012S			
	TA79015S			
	TA79018S			
	TA79020S		-40	
	TA79024S			
Power dissipation	(Ta = 25°C)	P _D	2	W
	(Tc = 25°C)		20	
Operating temperature		T _{opr}	-30~85	°C
Storage temperature		T _{stg}	-55~150	°C
Junction temperature		T _j	150	°C
Thermal resistance		R _{th(j-c)}	6.25	°C/W
		R _{th(j-a)}	62.5	

TA79012S

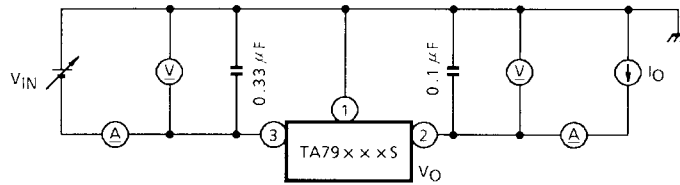
Electrical Characteristics

(Unless otherwise specified, $V_{IN} = -19\text{ V}$, $I_{OUT} = 500\text{ mA}$, $0^\circ\text{C} \leq T_j \leq 125^\circ\text{C}$, $C_{IN} = 0.33\text{ }\mu\text{F}$, $C_{OUT} = 0.1\text{ }\mu\text{F}$)

Characteristics	Symbol	Test Circuit	Test Condition	Min	Typ.	Max	Unit	
Output voltage	V_{OUT}	1	$T_j = 25^\circ\text{C}$	-12.5	-12.0	-11.5	V	
Line regulation	Reg-line	1	$T_j = 25^\circ\text{C}$	$-22\text{ V} \leq V_{IN} \leq -16\text{ V}$	—	13	120	mV
				$-30\text{ V} \leq V_{IN} \leq -14.5\text{ V}$	—	55	240	
Load regulation	Reg-load	1	$T_j = 25^\circ\text{C}$	$5\text{ mA} \leq I_{OUT} \leq 1.5\text{ A}$	—	46	240	mV
				$250\text{ mA} \leq I_{OUT} \leq 750\text{ mA}$	—	17	120	
Output voltage	V_{OUT}	1	$T_j = 25^\circ\text{C}$	-12.6	—	-11.4	V	
Quiescent current	I_B	1	$T_j = 25^\circ\text{C}$	—	4.4	8.0	mA	
Quiescent current change	Line	ΔI_B	$T_j = 25^\circ\text{C}$	$-30\text{ V} \leq V_{IN} \leq -14.5\text{ V}$	—	—	1.0	mA
	Load			$5\text{ mA} \leq I_{OUT} \leq 1.0\text{ A}$	—	—	0.5	
Output noise voltage	V_{NO}	2	$T_a = 25^\circ\text{C}$, $I_{OUT} = 20\text{ mA}$, $10\text{ Hz} \leq f \leq 100\text{ kHz}$	—	75	—	μV_{rms}	
Ripple rejection	R.R.	3	$f = 120\text{ Hz}$, $I_{OUT} = 20\text{ mA}$, $T_j = 25^\circ\text{C}$	54	61	—	dB	
Short circuit current limit	I_{SC}	1	$T_j = 25^\circ\text{C}$	—	1.9	—	A	
Dropout voltage	V_D	1	$T_j = 25^\circ\text{C}$, $I_{OUT} = 1.0\text{ A}$	—	2.0	—	V	
Average temperature coefficient of output voltage	T_{CVO}	1	$I_{OUT} = 5.0\text{ mA}$	—	1.6	—	$\text{mV}/^\circ\text{C}$	

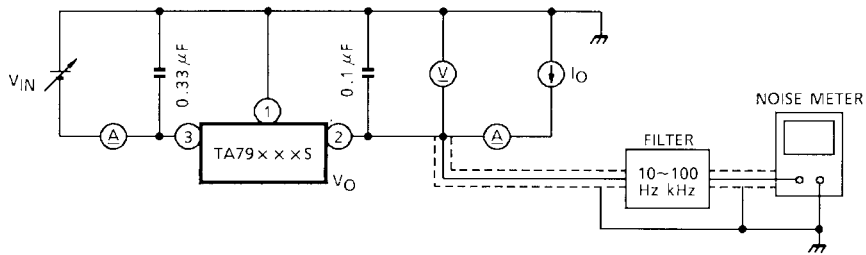
Test Circuit 1

V_{OUT} , Reg-line, Reg-load, I_B , ΔI_B , V_D , T_{cvo}



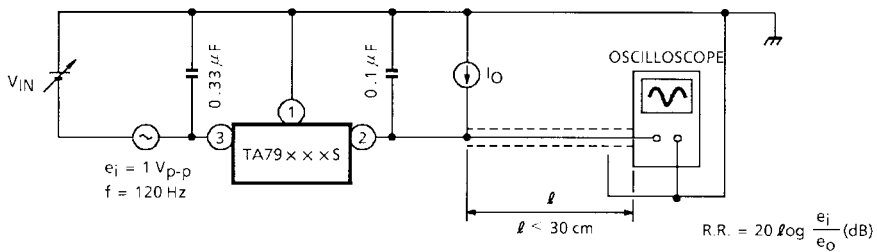
Test Circuit 2

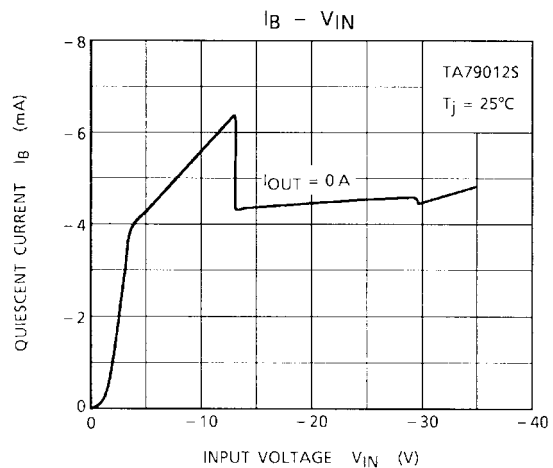
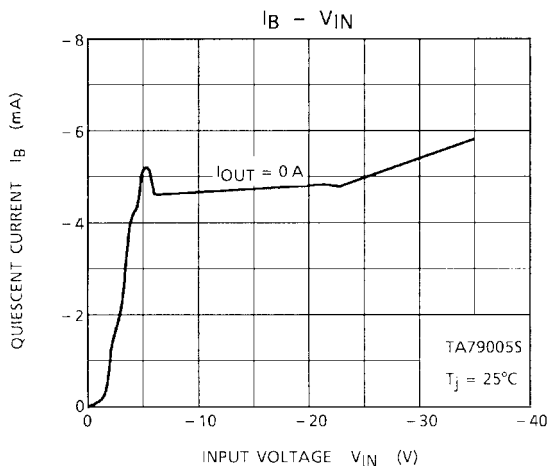
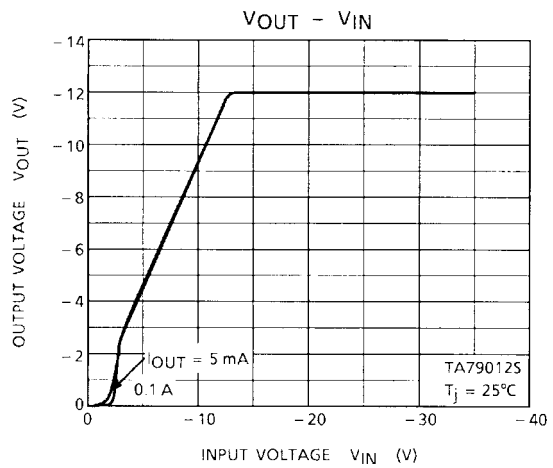
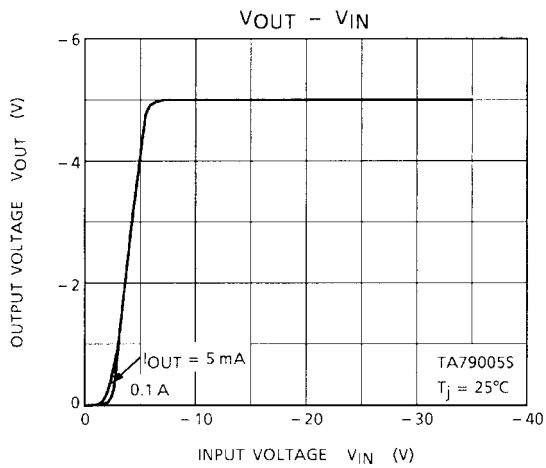
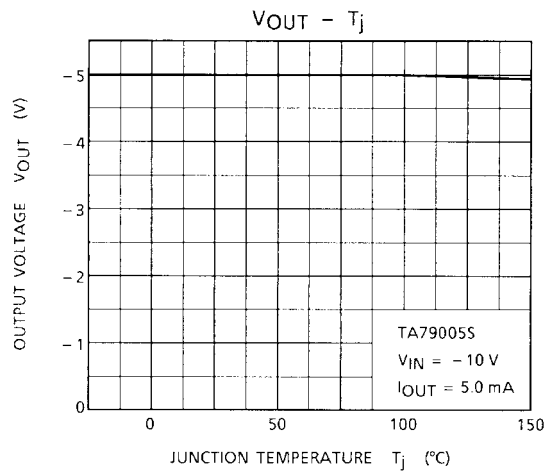
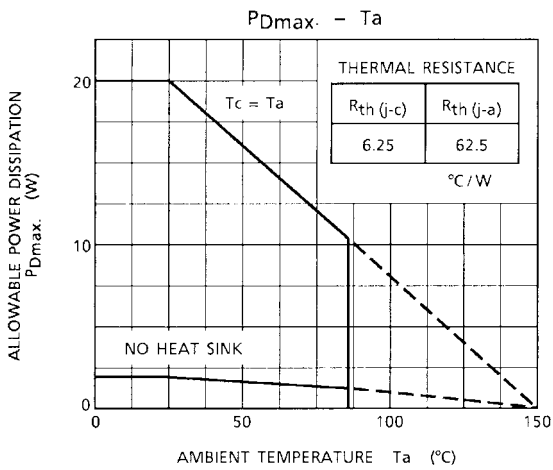
V_{NO}

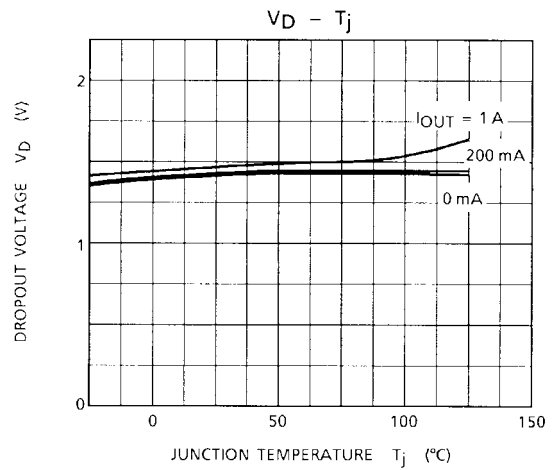
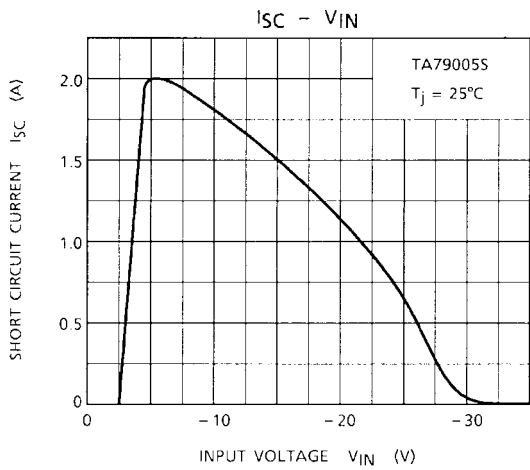
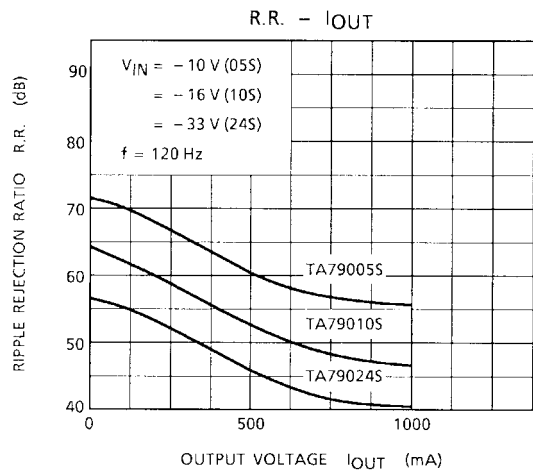
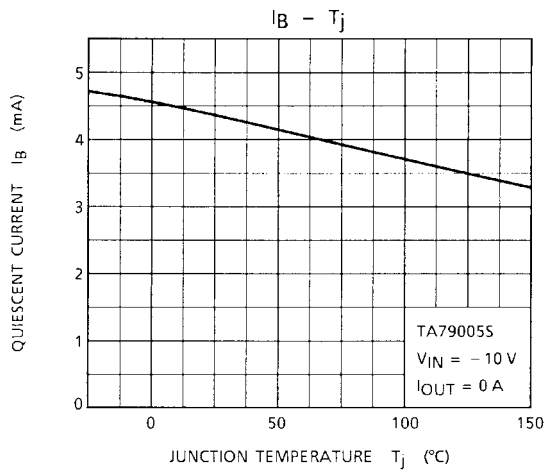


Test Circuit 3

R.R.



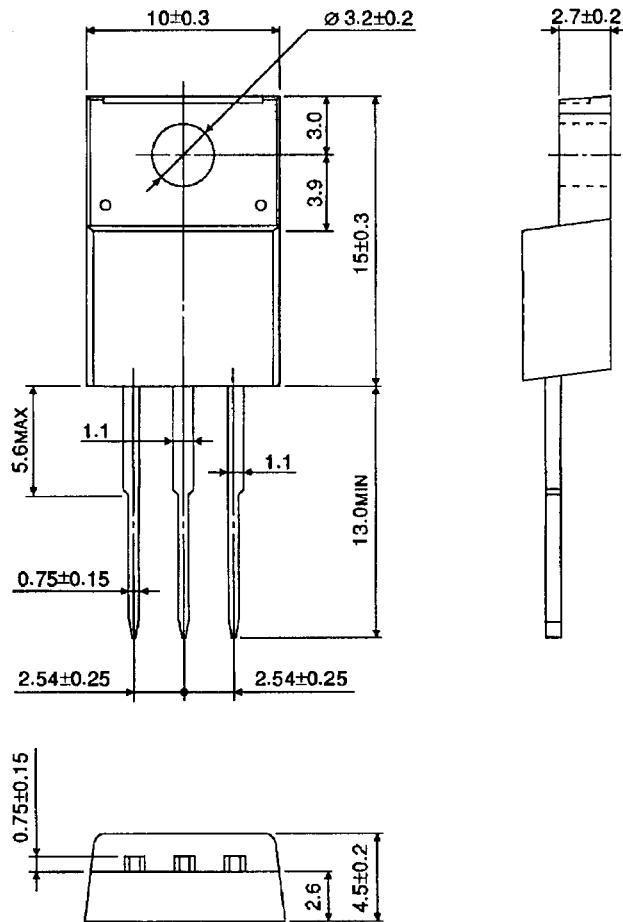




Package Dimensions

HSIP3-P-2.54A

Unit: mm



Weight: 1.7 g (typ.)